

Prior Art Fig. 1

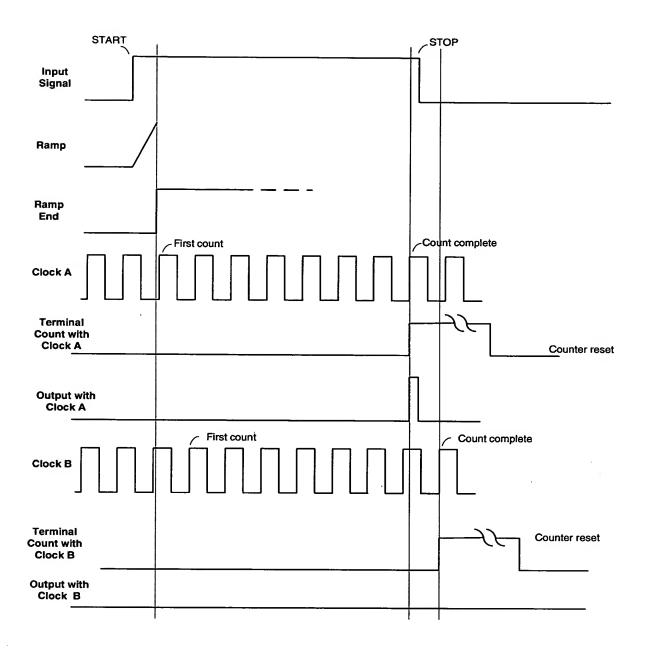
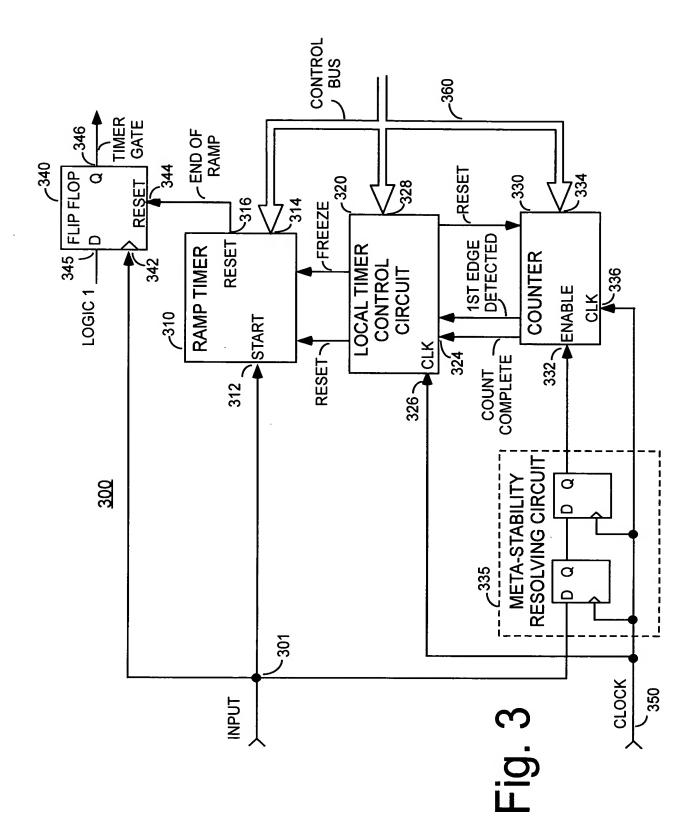


Fig. 2



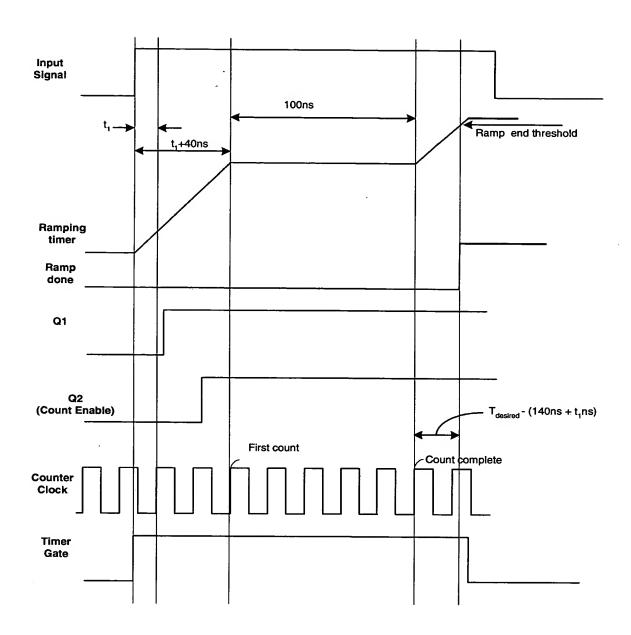
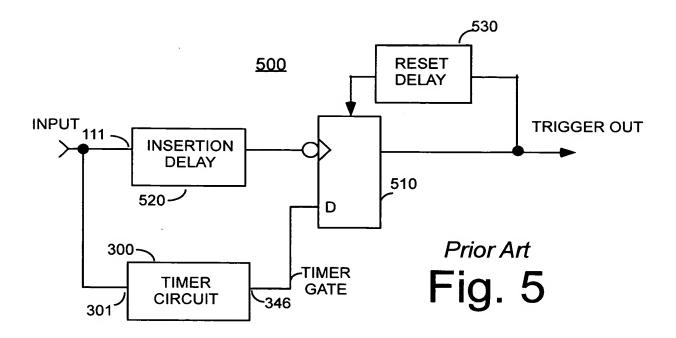
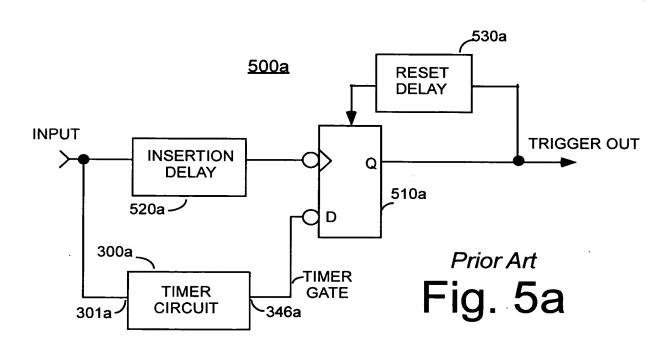


Fig. 4





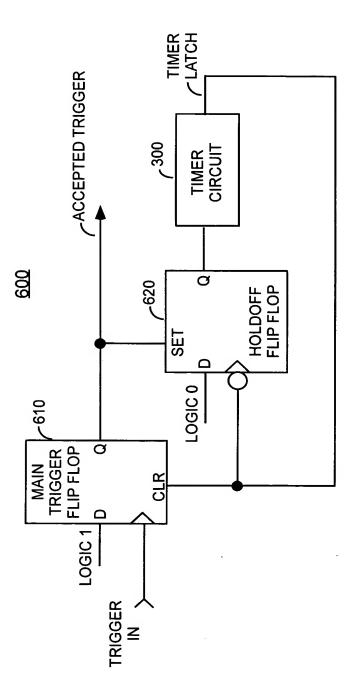


Fig. 6

SIGNAL AT CLK INPUT OF FLIP FLOP

TIMER CIRCUIT OUTPUT SIGNAL

Q OUTPUT OF FLIP FLOP

INPUT TO RESET OF FLIP FLOP

Fig. 7